

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

NAKAJYO, Shinsuke et al.

Serial No.: **09/686,958**

Filed: **October 12, 2000**

Group Art Unit: **2824**

Examiner: **Adam Pyonin**

P.T.O. Confirmation No.: 5708

For. **MANUFACTURE OF WAFER LEVEL SEMICONDUCTOR DEVICE AND
SEMICONDUCTOR DEVICE**

LETTER RE DRAWINGS

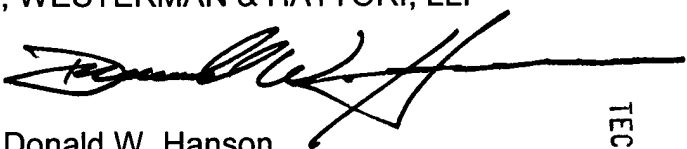
Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

With the concurrence of the examiner, please amend the drawings of the above identified application as shown in red in the attached copies of Figures 1-5. The amendments involve deleting the Japanese characters. If any fees are necessitated by the this Letter, such may be charged to Deposit Account 1-2340.

Respectfully submitted,

ARMSTRONG, WESTERMAN & HATTORI, LLP


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Enclosure: Figures 1-5

*6th letter to
the Draftsman*
August 29, 2002

9/10/02
DOBm